	Туре	L#	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	12976	first adj delay		2006/09/17 21:32
2	BRS	L2	18812	second adj delay		2006/09/17 21:33
3	BRS	L3	443	1 same 2 same parallel	US-PGPUB; USPAT	2006/09/17 21:34
4	BRS	L4	44643	(delay\$3 skew\$3) near5 (clock strobe)	US-PGPUB; USPAT	2006/09/17 21:35
5	BRS	L5	2566	1 same 4	US-PGPUB; USPAT	2006/09/17 21:35
6	BRS	L6	2461	2 same 4	US-PGPUB; USPAT	2006/09/17 21:36
7	BRS	L7	112	3 and 5 and 6	US-PGPUB; USPAT	2006/09/17 21:36
8	BRS	L8	35090	(adjust\$4 program\$5) near3 delay		2006/09/17 21:37
9	BRS	L9	48	7 and 8		2006/09/17 21:38
10	BRS	L10	2465	713/400-500.ccls.		2006/09/17 21:38
11	BRS	L11	1383	713/503.ccls. 713/600.ccls.		2006/09/17 21:39
12	BRS	L12	3270	10 or 11	US-PGPUB; USPAT	2006/09/17 21:39
13	BRS	L13	3	9 and 12	US-PGPUB; USPAT	2006/09/17 21:47
14	BRS	L14	45	9 not 13	US-PGPUB; USPAT	2006/09/17 21:47
15	BRS	L15	10642	(double adj data adj rate) or ddr	US-PGPUB; USPAT	2006/09/17 21:47
16	BRS	L16	17	14 and 15	US-PGPUB; USPAT	2006/09/17 22:02
17	BRS	L17	28	14 not 16	US-PGPUB; USPAT	2006/09/17 22:19
18	BRS	L18	7251	1.clm.	US-PGPUB; USPAT	2006/09/17 22:19
19	BRS	L19	7256	2.clm.	US-PGPUB; USPAT	2006/09/17 22:19
20	BRS	L20	700627	parallel.clm.	US-PGPUB; USPAT	2006/09/17 22:20
21	BRS	L21	11064	4.clm.	00.711	2006/09/17 22:20
22	BRS	L22	7219	8.clm.	00.711	2006/09/17 22:21
23	BRS	L23	42432	(clock adj signal).clm.	US-PGPUB; USPAT	2006/09/17 22:21

	Туре	L#	Hits	Search Text	DBs	Time Stamp
24	BRS	L24	71	18 and 19 and 20 and 21 and 22 and 23	US-PGPUB; USPAT	2006/09/17 22:23
25	BRS	L25	260	independent\$2 near3 (delay adj circuit\$1)		2006/09/17 22:25
26	BRS	L26	45111	sampling adj2 (signal data circuit)		2006/09/17 22:26
27	BRS	L27	42	25.clm.	US-PGPUB; USPAT	2006/09/17 22:27
28	BRS	L28	14594	26.clm.	US-PGPUB; USPAT	2006/09/17 22:27
29	BRS	L29	0	24 and 27 and 28		2006/09/17 22:28
30	BRS	L30	1	24 and 27	US-PGPUB; USPAT	2006/09/17 22:28